

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,079,596 B1
APPLICATION NO. : 09/535303
DATED : July 18, 2006
INVENTOR(S) : Yasuaki Namura

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title page, item (56), please insert the following U.S. patent documents:

-- 5,852,784 A * 12/1998 Ito et al.
5,966,666 A * 10/1999 Yamaguchi et al.
6,014,571 A * 1/2001 Enoki
6,026,307 A * 2/2000 Blom et al.
6,175,746 B1 * 1/2001 Nakayama et al.
6,393,299 B1 * 5/2002 Mizumoto et al. --

Please delete "Drawing Sheet 1 of 12", and insert therefor attached Drawing Sheet 1/12.

In Column 3, Line 44, please delete "demodulator" and insert therefor --modulator'--.

In Column 11, Line 41, please delete "demodulating" and insert therefor --modulating--.

In Column 22, Line 53, please delete "528", and insert therefor --518--.

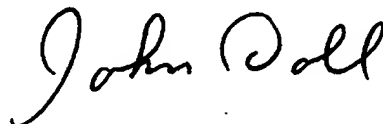
In Column 22, Line 60, please delete "574" and insert therefor --513--.

In Column 30, Lines 19-20, please delete "demodulator" and insert therefor --modulator--.

In Column 43, Claim 20, Line 9, please delete "chase" and insert therefor --phase--.

Signed and Sealed this

Twenty-sixth Day of May, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office

FIG. 1

